

and evacuating the inner space of the container through the vent tube.

With the present invention, the above-explained technical problems in the prior art can be solved and the foregoing objects can be achieved. With the manufacture method of the image-forming apparatus of the present invention, since the vent tube is disposed in a specific position, evacuation conductance can be increased to reduce an evacuation time. In addition, a higher vacuum level can be achieved in the container (envelope).

With the image-forming apparatus of the present invention, residual gas left in the container (envelope) space can be reduced to a very small amount and, therefore, stable image display can be continued for a long term.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a schematic perspective view, partly broken away, showing one example of the image-forming apparatus of the present invention.

Figs. 2 to 12 are schematic views for explaining some embodiments of the image-forming apparatus of the present invention.

Figs. 13A and 13B are schematic plan and sectional views, respectively, of a planar type surface conduction electron-emitting device which can be used

in the present invention.

Fig. 14 is a schematic view showing one example of a step type surface conduction electron-emitting device which can be used in the present invention.

5 Figs. 15A to 15C are schematic views showing successive manufacture steps of the surface conduction electron-emitting device.

10 Figs. 16A and 16B are charts showing examples of voltage waveforms which can be applied in the forming process by energization to manufacture the surface conduction electron-emitting device.

Fig. 17 is a schematic view showing an FE electron-emitting device.

15 Fig. 18 is a schematic view showing one example of a base plate for an electron source in a matrix pattern.

Figs. 19A and 19B are schematic views showing examples of a fluorescent film.

20 Fig. 20 is a block diagram showing one example of a driving circuit adapted to display an image in accordance with TV signals of NTSC standards.

Fig. 21 is a schematic view showing one example of a base plate for an electron source in a ladder pattern.

25 Fig. 22 is a schematic view of a typical surface conduction electron-emitting device.

Fig. 23 is a schematic view showing a conventional